

About the Institution

Kings College of Engineering (KCE) is an institution which was formed with the single aim of providing quality education to the poor and underprivileged students. KCE is approved by All India Council for Technical Education (AICTE), New Delhi and is affiliated to Anna University, Chennai. The institution was established in the year 2001 on a sprawling campus of around 60 acres on the Thanjavur - Pudukkottai Highway and run by Raj Educational Trust (RET), Chennai. The institution offers five UG and five PG programmes, certified with ISO 9001:2008 and accredited by NAAC. The state of the art teaching methodologies are used to impart high quality education to the students including e-learning.

About the Department

The Department of Electronics and Communication Engineering was established with state of art laboratories in the year 2001. It is continuously upgrading the infrastructure and laboratory facilities with the emerging technology as per the industrial needs. The department is recognized as research centre for collaborative research by Anna University, Chennai. A significant number of research projects funded by government and non-government organizations viz. DRDO, MCIT, AICTE New Delhi, TNSCST, Chennai and Texas Instruments USA have been successfully carried out.

About the Workshop

The workshop on "CAD for VLSI" is an acquaintance tool in learning new technique in CAD tools and their scientific advancement in the emerging directions for the department of ECE and EEE. This workshop involves introduction about VLSI, Different stages in VLSI design flow, Front-end VLSI design, FPGA Design flow, Transistor level design issues, Logic synthesis and static timing Analysis, High speed circuits and processor architectures, ASIC design flow.

Forenoon Session : Introduction to VLSI,
Basics of ASIC/FPGA Design Process,
CAD Tools,
Recent Trends in VLSI
Afternoon Session : Hands on Training.

NATIONAL LEVEL WORKSHOP ON CAD for VLSI

9th March 2018

REGISTRATION FORM

Name : _____

Qualification : _____

Department : _____

Organization : _____

Mailing Address _____

Phone No. : _____

E-mail : _____

Registration fee details:

Amount Rs. _____

Place :

Date :

Signature of Applicant

Organizing Committee

Chief Patron

Dr. R. Rajendran

Secretary

Mr. T.R.S. Muthukumaar M.B.A.,

Chief Executive Officer

Patron

Dr. J. Arputha Vijaya Selvi, B.E., M.E., P.hD.,

Principial, Kings College of Engineering.

Convener

Mrs. N. Mangaiyarkarasi, B.E., M.E.,

Academic Coordinator, ECE

Resource Persons

Session 1 : Dr. M. Joseph M.E., Ph.D(NITK),

Principal St.Joshep college of Engineering and Technology.

Session 2 : Mr.T.Pasupathi M.E.,(P.hD),

Assistant Professor, Kings College of Engineering.

Organizing Secretaries

Mr.W.NewtonDavidRaj,B.E.,M.Tech.,M.B.A.,M.Div., AP/ECE

9751818592

Mr.P.Raja Pirian, M.E.,M.B.A.,M.Div., (P.hD),AP/ECE

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Registration Fee

UG students/PG students/Faculty Members/ Research Scholars : Rs. 200/-

For IEEE Members : Rs. 100/-

(Reg fee includes registration kit and Lunch)

Important Dates

Last date of Registration

: 07-03-18



National Level Workshop on CAD for VLSI

9th March, 2018



Organized by

Department of Electronics and Communication
Engineering

Jointly Organized by
IEEE Student Branch

KINGS COLLEGE OF ENGINEERING

(NAAC Accredited Institution)

Approved by AICTE, New Delhi & Affiliated to Anna University, Chennai

Punalkulam, Gandarvakkottai Taluk,

Pudukkottai Dist. Pin-613 303, TamilNadu.

Phone : 04362-282474

REPORT

One Day Workshop on CAD for VLSI

CO ordinators

Mr.W.Newton David Raj AP/ECE

Mr.P.Raja Pirian AP/ECE

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
KINGS COLLEGE OF ENGINEERING PUNALKULAM

One day work shop on CAD for VLSI organized by department of Electronics and communication engineering, to motivate the students to involve them in VLSI Domain and also the students get more ideas about tools used in CAD. Dr.M.Joseph, Principal, St.Joseph College of Engineering and Technology, Thanjavur & Mr.T.Pasupathi AP/ECE, Kings college of Engineering were the resource persons.



Presidential Address By Mrs.T.Shanthi



Honoring the Chief Guest



Participants



Chief guest Address by Dr.M.Joseph



Hands on session by Mr.T.Pasupathi



Hands on session at VLSI Lab

The workshop was begins at 10.10a.m, Mr.P.Raja Pirian AP/ECE welcomes the resource person and the gathering, followed by Mr.W.Newton David Raj AP/ECE introduced the chief guest, Mrs.T.Shanthi, Project officer delivered the inaugural address after that the chief guest was honored by Mrs.T.Shanthi. Finally Dr.M.Joseph delivered the chief guest address. Immediately the 1st session begins with a Lecture on Introduction to CAD for VLSI by Dr.M.Joseph. After the Lunch break The 2nd session was continued by Mr.T.Pasupathi AP/ECE at VLSI lab with CAD tools. The students were actively involved in the entire sessions. The program ends with valedictory.

Nearly 41 students were participated in the workshop; 28 students from our college and the remaining participants from ksk Engineering college and vandaiyar engineering college.